TO

# PATENT APPLICATION

042390.P11448

915036843245

### Amendments to the Claims

1.(currently amended) A method to reduce initialization time of a system, comprising:

storing at least a portion of the data accessed during initialization of the system in a non-volatile cache memory of the system; and

pinning [[a]] at least a portion of the data stored in the non-volatile cache memory, wherein the pinning is performed during initialization of the system.

- 2.(currently amended) The method of claim 1, wherein storing the data comprises storing the data in a mass storage non-volatile cache memory.
- 3.(currently amended) The method of claim 1, wherein pinning of data comprises pinning the portion of data necessary for a system initialization.

TO 91<u>52</u>36843245

4.(currently amended) The method of claim 1, wherein the pinning of data comprises:

storing metadata corresponding to the data stored in the non-volatile cache memory; and

setting a state in the metadata to indicate that a corresponding line of data is pinned.

- 5.(original) The method of claim 4, wherein storing the metadata comprises storing the metadata in a second memory.
- 6.(original) The method of claim 4, wherein storing the metadata comprises storing the metadata in a volatile storage media.
- 7.(currently amended) A metadata stored in-a memory <u>a system</u> comprising:
- a first state to indicate a least recently used information of a corresponding line of initialization data in a non-volatile memory of the system; and
- a second state to indicate whether a corresponding line of <u>initialization</u> data in the non-volatile memory is pinned.

8.(currently amended) The metadata of claim 7, further comprising:

a third state to indicate whether a corresponding line of <u>initialization</u> data in the non-volatile memory was present before a system initialization <u>of the system</u>.

Bl

9.(currently amended) The metadata of claim 7, wherein the metadata is stored in a volatile storage media, wherein the second state is set during initialization of the system.

10.(currently amended) A system comprising:

a cache including a first storage media to store cache data <u>accessed</u> during initialization of the system, the first storage media being a non-volatile storage media; and

a second storage media to store metadata for the cache data stored in the first storage media, the metadata including a state to indicate whether a corresponding line of data is pinned, wherein the state is set during initialization of the system.

11.(original) The system of claim 10, wherein the cache is a mass storage cache.

TO

915036843245

#### PATENT APPLICATION 042390.P11448

12.(original) The system of claim 10, wherein the second storage media is a volatile storage media.

13.(original) The system of claim 10, wherein the second storage media is included in the cache.

14.(original) The system of claim 10, wherein the cache is implemented as an add-in card.

15.(currently amended) A method comprising:

accessing a first memory during a system initialization, the first memory being a cache; and

pinning data accessed during the system initialization in the first memory. wherein the pinning occurs during the system initialization.

16.(currently amended) The method of claim 15, wherein the cache is a mass storage non-volatile cache.

TO 91<u>50</u>36843245

17.(original) The method of claim 15, further comprising:

limiting the pinning of data during the system initialization.

18.(currently amended) The method of claim 15, wherein the pinning of data during the system initialization comprises:

storing metadata for the data stored in the first memory, the metadata including a first state to indicate whether a corresponding line of data is pinned; and

setting a first state corresponding to the accessed data to indicate that the accessed data is pinned.

19.(original) The method of claim 18, wherein the pinning of data further comprises:

setting a timer upon the system initialization; and

setting a first state corresponding to the accessed data until the timer expires.

20.(original) The method of claim 18, wherein the pinning of data further comprises:

setting a maximum amount of data to pin; and

setting a first state corresponding to the accessed data until the maximum amount is exceeded.

21.(original) The method of claim 18, wherein the metadata further includes a second state; and wherein the pinning of data further comprises:

setting a second state for data that was present before system initialization, the setting of the second state to indicate that a corresponding data was present before the system initialization;

setting a timer upon the system initialization;

setting a maximum amount of data to pin;

setting a first state corresponding to the accessed data if the maximum amount is not exceeded and if the timer has not expired; and otherwise

clearing a first state corresponding to a pinned data and setting a first state corresponding to the accessed data if the second state corresponding to the pinned data is not set and the pinned data corresponding to the accessed data is set, and if the timer has not expired.

FROM

INTEL

TO

915036843245

22.(original) The method of claim 21, wherein the metadata further includes a third state to indicate the age of a corresponding line of data and the clearing of a first state comprises:

clearing the latest line of data if there is more than one line of pinned data whose second state is not set.

23.(currently amended) A system comprising:

a cache including a first storage media to access during a system initialization, the first storage media being non-volatile;

a second storage media to store metadata for data accessed during the system initialization, the metadata including a first state; and

a memory control hub to cause [[a]] the first state to be set for data accessed during the system initialization, the setting of the first state to occur during the system initialization and to indicate that a corresponding line of data is pinned.

24.(original) The system of claim 23, wherein the metadata further includes a second state; and wherein the memory control hub causes the second state to be set for data present before the system initialization, the setting of the second state to indicate that a corresponding line of data was present before the system initialization.

25.(original) The system of claim 23, wherein the cache is a mass storage cache.

26.(original) The system of claim 23, wherein the memory control hub limits the amount of data pinned.

B/

27.(original) The system of claim 23, wherein the second storage media is a volatile storage media.

28.(original) The system of claim 23, wherein the second storage media is included in the cache.

29.(original) The system of claim 23, wherein the cache is implemented as an add-in card.

30.(currently amended) A program loaded into a computer readable media comprising:

a first group of computer instructions to access data in a non-volatile cache of a system;

a second group of computer instructions to pin data accessed in the non-volatile cache, during initialization of the system.

31.(original) The program of claim 30, wherein the second group of computer instructions includes computer instructions to pin data accessed during a system initialization.

B/

32.(original) The program of claim 31, wherein the second group of computer instructions further includes computer instructions to limit the amount of data pinned.

33.(currently amended) A method to reduce initialization time of a system, comprising:

preventing eviction of data stered in a non-volatile cache memory

marking data stored in a non-volatile cache memory to prevent eviction of the data, wherein the marking occurs during initialization of the system.

34.(currently amended) The method of claim 33, wherein preventing marking comprises preventing eviction of cache marking data stored in a mass storage non-volatile disk cache memory.

TO

915236843245

#### PATENT APPLICATION 042390.P11448

35.(currently amended): The method of claim 33, wherein preventing marking comprises[[:]] marking data accessed during initialization of the system and further comprising storing eache the data accessed during initialization in the non-volatile cache memory; and

marking the eache data to prevent eviction of the eache data.

36.(currently amended) The method of claim [[35]] 33, wherein marking the data includes storing metadata corresponding to the eache data in a second memory different than the non-volatile cache memory, wherein the metadata includes a pinned bit that is set by a memory control hub to prevent eviction of the corresponding cache data stored in the non-volatile cache memory.

- 37.(new) The method of claim 33, further comprising limiting the amount of data that is marked using either a timer or a maximum count.
- 38.(new) The method of claim 33, wherein marking comprises setting at least one bit to indicate that the data is pinned.
- 39.(new) The method of claim 38, further comprising storing the at least one bit in a volatile memory.

40.(new) The method of claim 38, further comprising storing the at least one bit in the non-volatile cache memory, wherein the non-volatile cache memory is a non-volatile disk cache memory.

41.(new) The method of claim 33, wherein the marking occurs during each initialization of the system.